



Design and Realization of Bipolar Transistors (Design And Measurement in Electronic Engineering)

Peter Ashburn

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Addresses new developments in the design and fabrication of bipolar transistors for high-speed digital circuits. Covers advances in silicon technology (such as polysilicon emitters and self-aligned fabrication techniques), gallium arsenide technology (such as extremely high-performance MSI circuits resulting from the development of GaAs/GaAlAs heterojunctions), and new applications of bipolar transistors (such as optoelectronic circuits). Also deals with optimization of bipolar devices and processes for high-speed, digital circuits by means of a quasi-analytical expression for the gate delay of an ECL logic gate. Includes case studies.

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